

IN THE CLAIMS

Cancel claims 1-12 without prejudice or disclaimer, and add new claims 13-26 as follows.

1-12. (Canceled).

13. (New) An IC chip comprising:

a voltage conversion circuit;

a level conversion circuit;

a voltage limiter; and

an information processor

wherein said voltage conversion circuit decreases a first supply voltage from a contact point which connects a reader/writer and applies said decreased first supply voltage to said information processor;

said level conversion circuit decreases a signal level of a signal from said contact point and inputs said signal of said decreased signal level to said information processor;

said voltage limiter limits a second supply voltage from an antenna which communicates with another reader/writer and inputs said limited second supply voltage to said information processor; and

said decreased first supply voltage is less than or equal to a limit voltage of said voltage limiter.

14. (New) The IC chip according to claim 13,
wherein said level conversion circuit raises a
signal level of a signal from said information processor and
inputs said signal of said raised signal level to said contact
point.

15. (New) The IC chip according to claim 13, further
comprising:

a selector which selects a signal from either said
contact point or said antenna.

16. (New) The IC Chip according to claim 15, further
comprising:

a voltage detector which detects an input of either
said first supply voltage or said second supply voltage and
controls said selector according to a result of the detection.

17. (New) The IC chip according to claim 13,
wherein said limit voltage of said voltage limiter
is a withstand voltage of said information processor.

18. (New) The IC chip according to claim 13, further
comprising:

an RF interface which demodulates a signal received from said antenna and modulates data produced in said information processor.

19. (New) An IC card which incorporates the IC chip according to claim 13.

20. (New) An IC chip comprising:

a voltage conversion circuit;

a level conversion circuit;

a series regulator; and

an information processor;

wherein said voltage conversion circuit decreases a first supply voltage from a contact point and applies said decreased first supply voltage to said information processor;

 said level conversion circuit decreases a signal level of a signal from said contact point and inputs said signal of said decreased signal level to said information processor;

 said series regulator limits a second supply voltage from an antenna and inputs said limited supply voltage to said information processor; and

 said decreased first supply voltage is less than or equal to a limit voltage of said series regulator.

21. (New) The IC chip according to claim 20,
wherein said level conversion circuit raises a
signal level of a signal from said information processor and
inputs said signal of said raised signal level to said contact
point.

22. (New) The IC chip according to claim 20, further
comprising:

a selector which selects a signal from either said
contact point or said antenna.

23. (New) The IC chip according to claim 22, further
comprising:

a voltage detector which detects an input of either
said first or said second supply voltage and controls said
selector according to a result of the detection.

24. (New) The IC chip according to claim 20,
wherein said limit voltage of said series regulator
is a withstand voltage of said information processor.

25. (New) The IC chip according to claim 20, further
comprising:

an RF interface which demodulates a signal received from said antenna and modulates data produced in said information processor.

26. (New) An IC card which incorporates the IC chip according to claim 20.